

ABSTRACT OF THE DISCLOSURE

A circuit for providing a pulse to drive a capacitive load comprises (a) a first inductive component that influences both a transition time of a rising edge of the
5 pulse and a transition time of a falling edge of the pulse, and (b) a second inductive component that influences one of the transition time of the rising edge and the transition time of the falling edge so that the rising edge and the falling edge are asymmetrical.